

the controlled voltage waveform is added to the common electrode signal, generating a corrected common electrode signal (col. 4, lines 9-52).

Thus, Moon cannot provide a display signal that includes the predetermined signal and the multiple pixel signals. Moreover, in Moon, a display signal is not combined with a predetermined signal, which is used for generating a common signal, but the display signal is integrated, sampled and held, and the voltage waveform obtained through gain controlling is only added to the common electrode signal. Thus, the voltage waveform obtained from the display signal is different from the display signal itself.

Thus, Moon fails to disclose all of the features of claims 16, 17 and 20. As such, it is respectfully submitted, claims 16, 17 and 20 are distinguishable over the applied art. Furthermore, claim 18, which depends from claim 17, is likewise distinguishable over the applied art for at least the reasons discussed above, as well as for the additional features it recites. Accordingly, withdrawal of the rejection under §102 is respectfully requested.

III. Conclusion

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact Applicant's undersigned representative at the telephone number listed below.

Respectfully submitted,


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JAO:JLC/aaw

Attachment:
Figure

Date: August 5, 2004

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